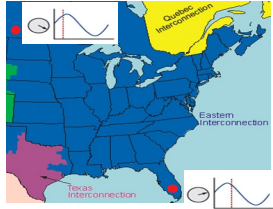


## GOALS

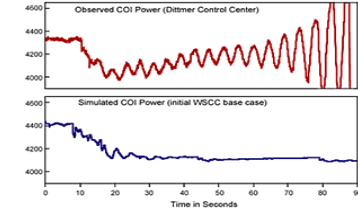
- Build & test an open-box PMU to understand the challenges of measuring, processing, synchronizing, and integrating synchrophasor data.
- Investigate synchrophasor data quality issues and identify remedies.

## FUNDAMENTAL QUESTIONS/CHALLENGES



Accurately compare synchronized measurements at distant points

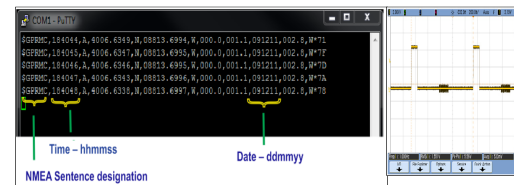
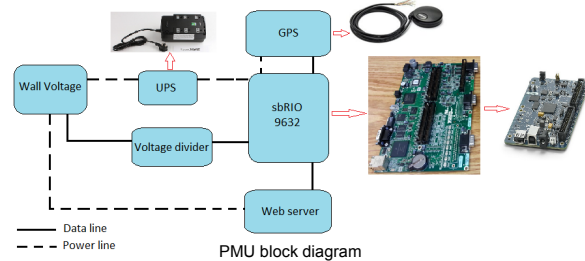
- Substantially reduce PMU costs (which now are well over \$1,000 per device).
- Improve PMU performance to meet next-generation synchrophasor measurement requirements.
- No PMU fully complies with established IEEE standards<sup>1</sup>.



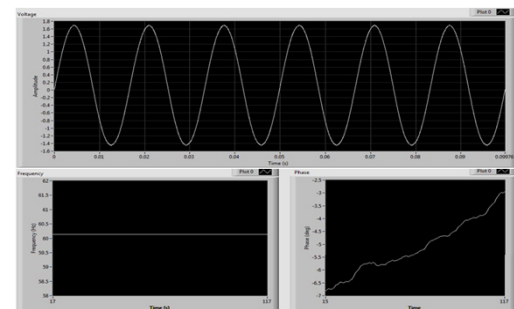
Refining system models – observed vs. simulated conditions

## RESEARCH RESULTS

- Wall voltage sampled at 10 kHz; computes 10 phasor data sets/sec.
- Synchrophasor timestamp is derived from GPS timing signal/data.
- Data are buffered and transferred each hour to a remote Linux web server via FTP connection.
- Standalone PMU implemented on NI's sbRIO-9632.



GPS time data (left) and second pulse (right)



Voltage, frequency, and phase data

Date	Time	Phase	Vrms	Frequency
08/09/13	7:01:1900000	62.14	125.15	59.9910
08/09/13	7:01:2100000	61.79	125.15	59.9922
08/09/13	7:01:2300000	61.51	125.15	59.9927
08/09/13	7:01:2500000	61.24	125.14	59.9908
08/09/13	7:01:2700000	60.91	125.15	59.9930
08/09/13	7:01:2900000	60.69	125.15	59.9924
08/09/13	7:01:3100000	60.41	125.08	59.9904
08/09/13	7:01:3300000	60.09	125.08	59.9935
08/09/13	7:01:3500000	59.81	125.08	59.9917
08/09/13	7:01:3700000	59.53	125.09	59.9902
08/09/13	7:01:3900000	59.17	125.12	59.9919
08/09/13	7:01:4100000	58.86	125.16	59.9905
08/09/13	7:01:4300000	58.53	125.16	59.9916

Sample synchrophasor data stream for upload to a web server

## Conformance Test Results

PMU	Class	Steady State Test								Dynamic State Test								Step Change	
		Magnitude Variation				Phase Angle Variation				Frequency Variation				Measurement Fluctuation					
		TVE	FE	RE	DFE	TVE	FE	RE	DFE	TVE	FE	RE	DFE	TVE	FE	RE	DFE		
A	S2	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N
A1**	S1	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N
	S2	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N
B	S1	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N
C	S1	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N
D	S1	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N
E	S1	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N
F	S1	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N
G	S1	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N
H	S1	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N	N

<sup>1</sup> PMU A-1 is an upgraded firmware of PMU A. P: Class P; M: Class M.  
Y/N: total vector error; FE: frequency error; RFE: rate of change of frequency error;  
RT: response time; DT: delay time; SD: standard deviation; S: standard for "Standard" 1 stands for "Failed".

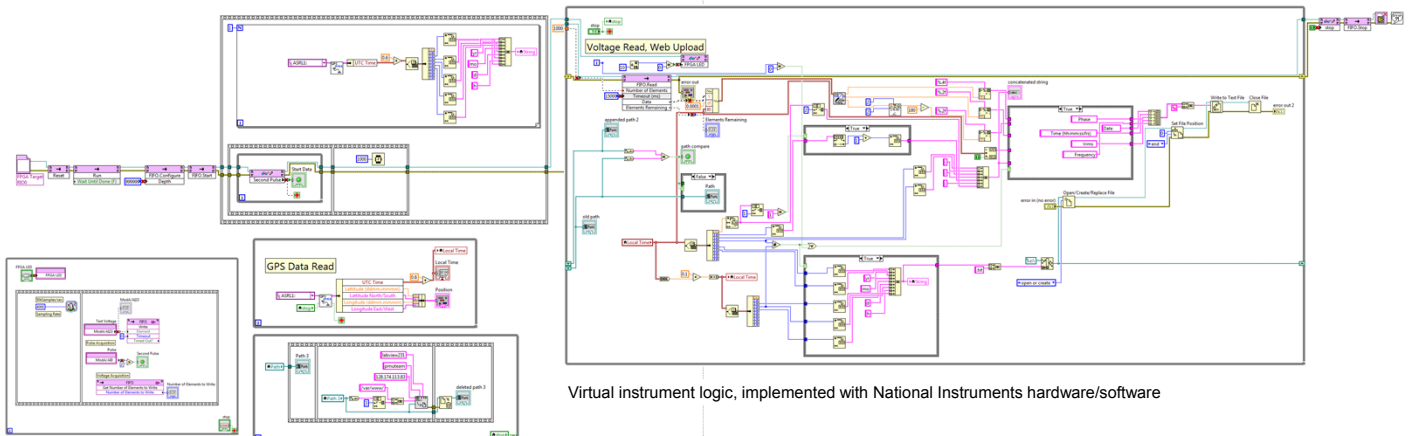
<sup>1</sup> Mladen Kezunovic, "Verifying Interoperability and Application Performance of PMUs and PMU-enabled IEDs at the Device and System Level," North American Synchrophasor Phasor Initiative Working Group Meeting, 5 Jun 12, Denver, CO.

## RESEARCH PLAN

- Complete PMU integration with an uninterruptible power supply (UPS), enabling synchrophasor data collection during power interruptions.
- Explore next-generation PMU device requirements.
- Develop and implement new algorithms for next-generation PMUs.

## BROADER IMPACT

- Gain detailed understanding of PMU measurement challenges, supporting overall synchrophasor data quality activity research.
- Substantially reduce PMU per-unit cost to ~\$250.
- Install low-cost PMUs supporting distribution system research.



Virtual instrument logic, implemented with National Instruments hardware/software

## INTERACTION WITH OTHER PROJECTS

- Apply the experience and insights gained to investigate synchrophasor data quality in real-world power systems.
- Provide our PMU to the TCIPG testbed to assess vulnerabilities to GPS signal spoofing.

## FUTURE EFFORTS

- Improve our PMU's performance to fully comply with IEEE standards.
- Implement security measures to safeguard data (e.g., encryption).
- Apply knowledge gained to improve synchrophasor data quality.